

FIG. 1

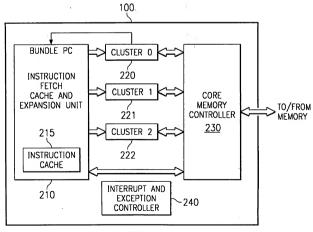


FIG. 2

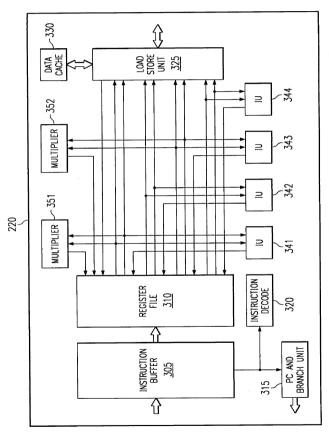
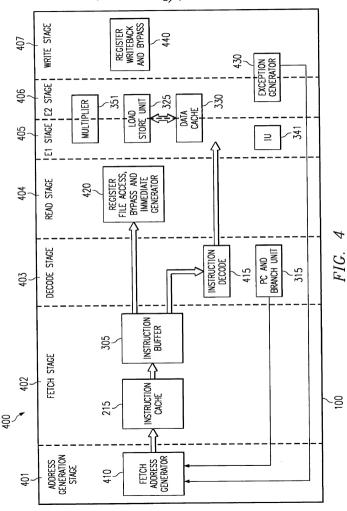
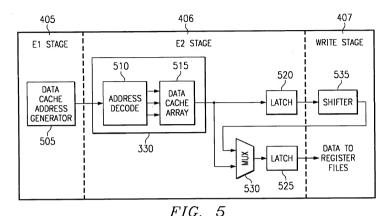


FIG. 3





START 100 DURING LOAD OPERATIONS, DATA IS FETCHED 605 FROM DATA CACHE 330 IN TWO CYCLES FOR LOAD WORD OPERATION, DATA BYPASSES SHIFTER 535 VIA MUX 530 AND LATCH 535 AND 610 IS TRANSFERRED TO REGISTER FILE OR OTHER PIPELINE STAGE AT END OF SECOND CYCLE FOR LOAD HALF-WORD/LOAD BYTE OPERATION, DATA IS TRANSFERRED THROUGH SHIFTER 535; SHIFTED DATA IS TRANSFERRED VIA MUX 530 615-AND LATCH 535 TO REGISTER FILE OR OTHER PIPELINE STAGE AT END OF THIRD CYCLE

CONTINUE FIG. 6

600